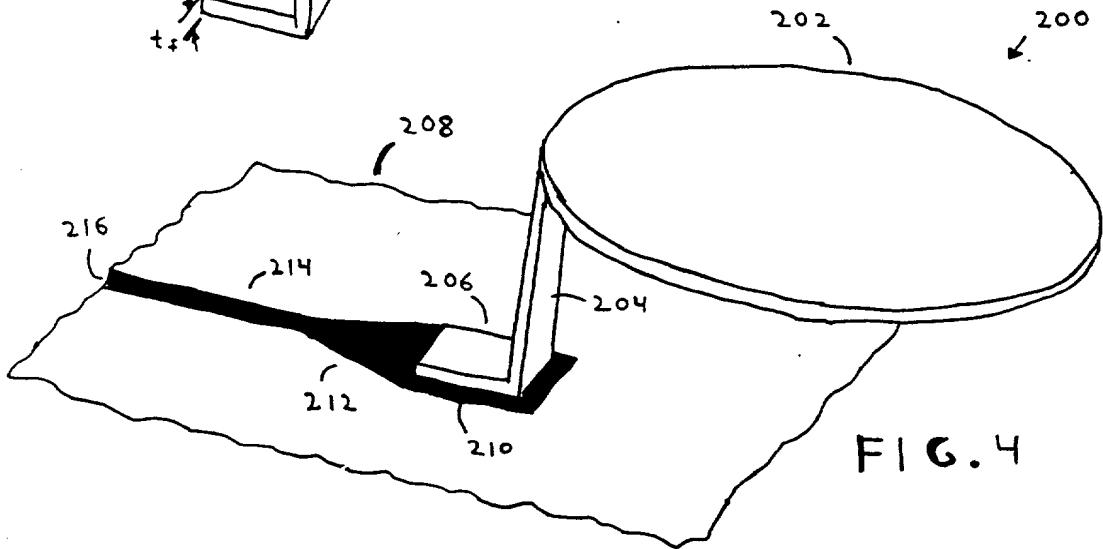
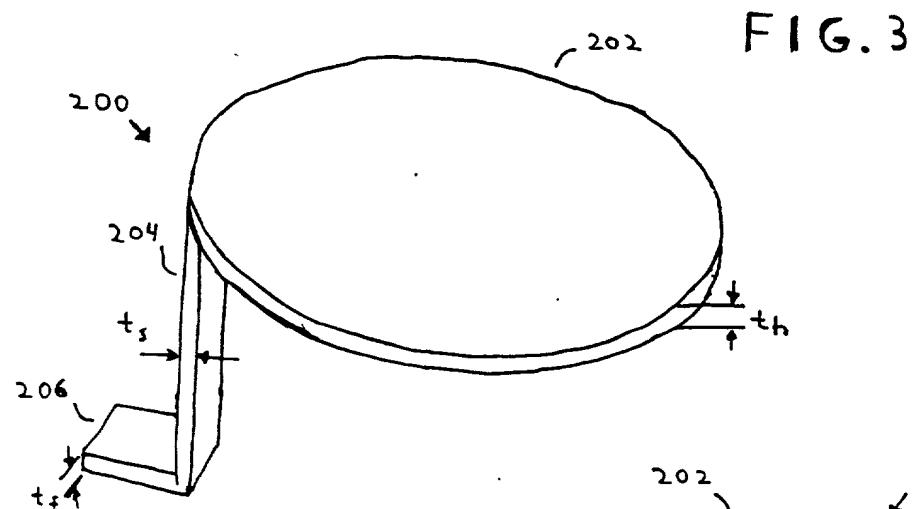
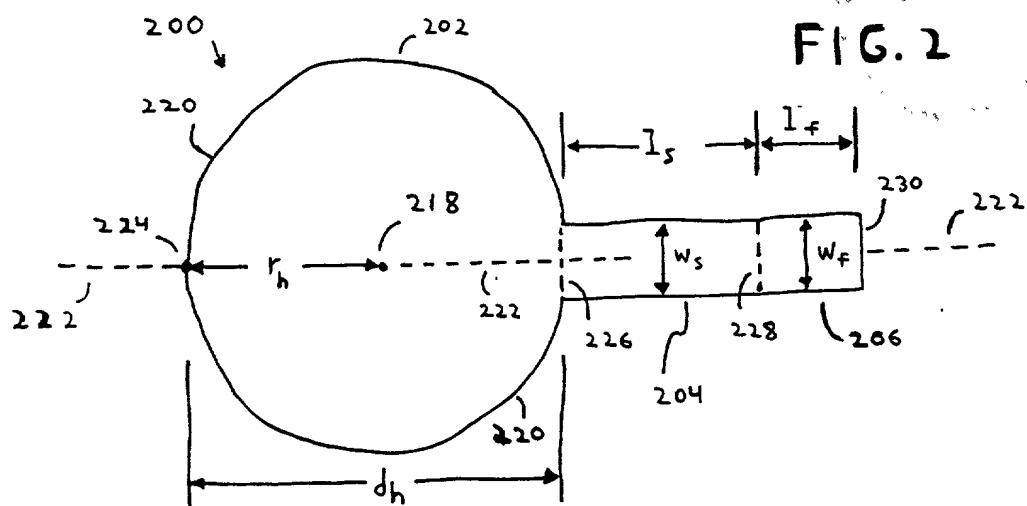


FIG. 1 PRIOR ART



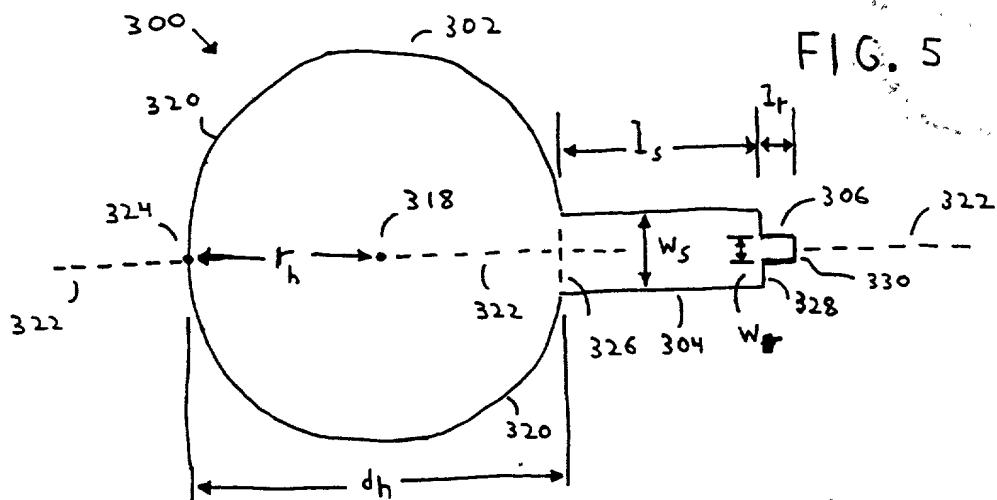
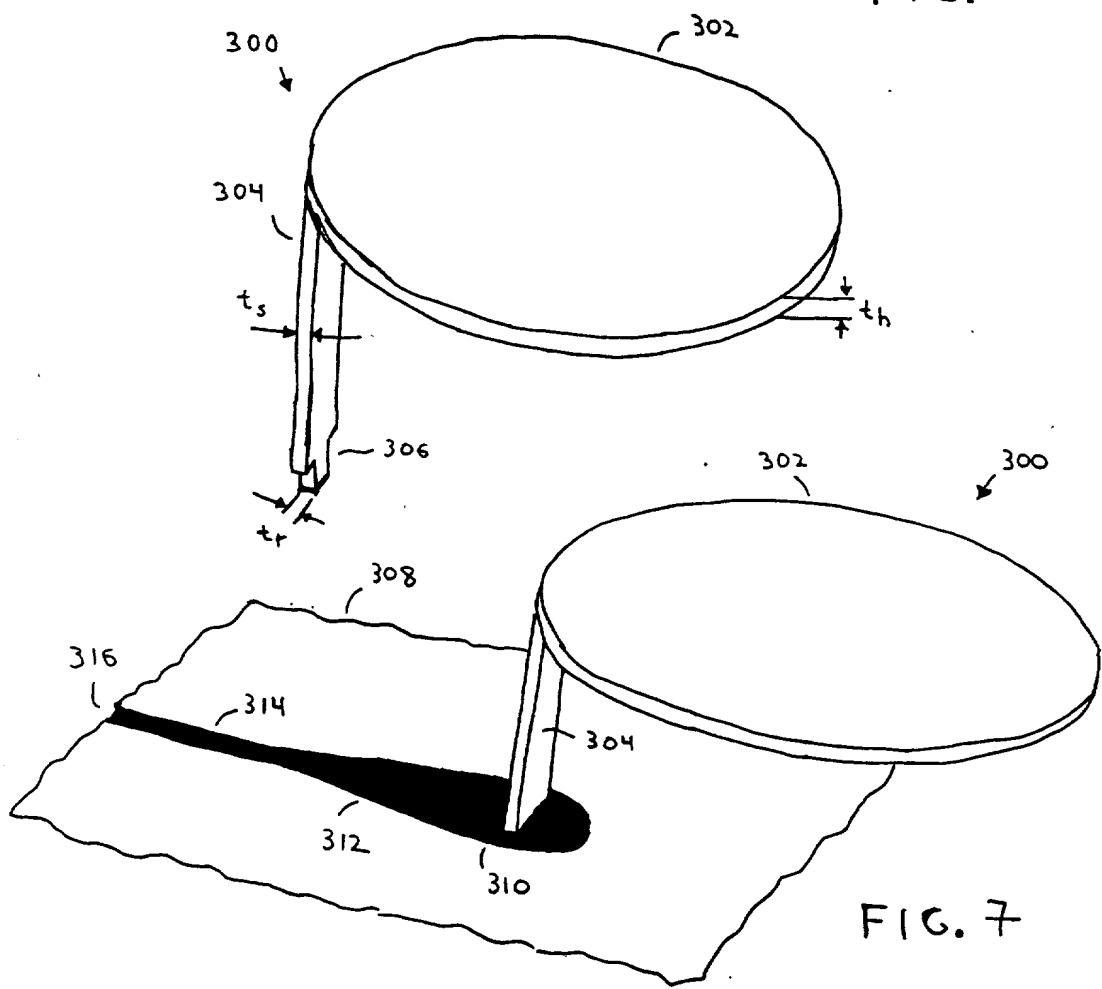


FIG. 6



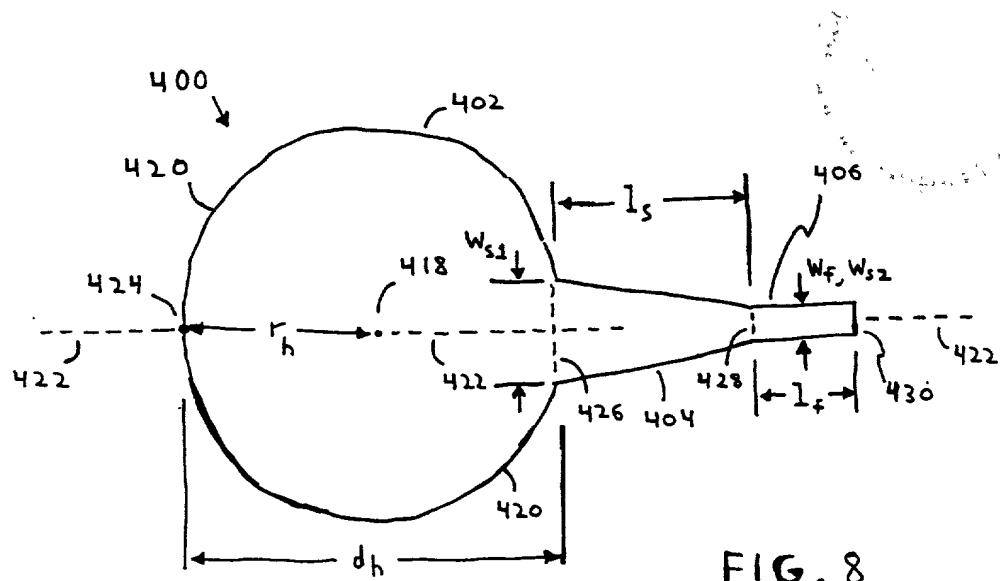


FIG. 8

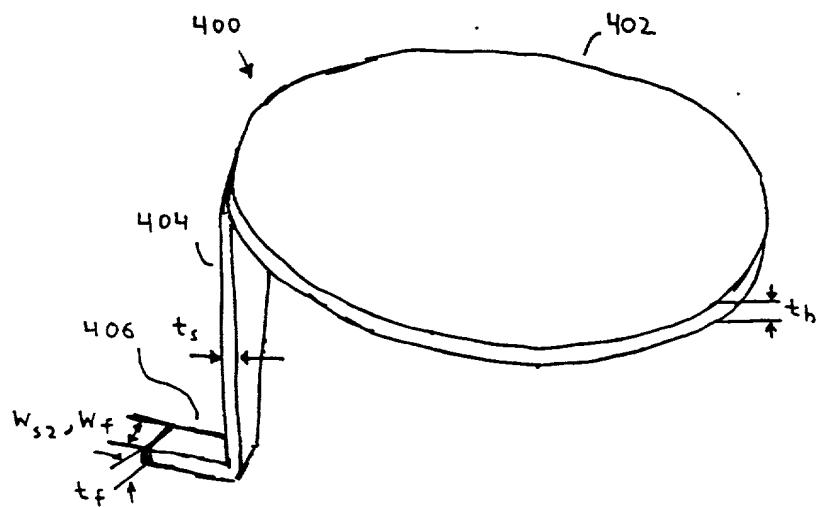


FIG. 9

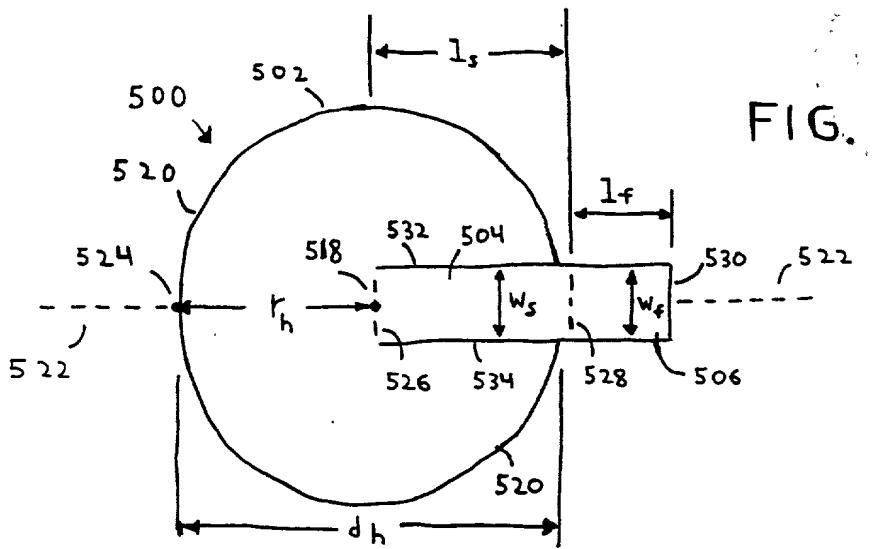


FIG. 10

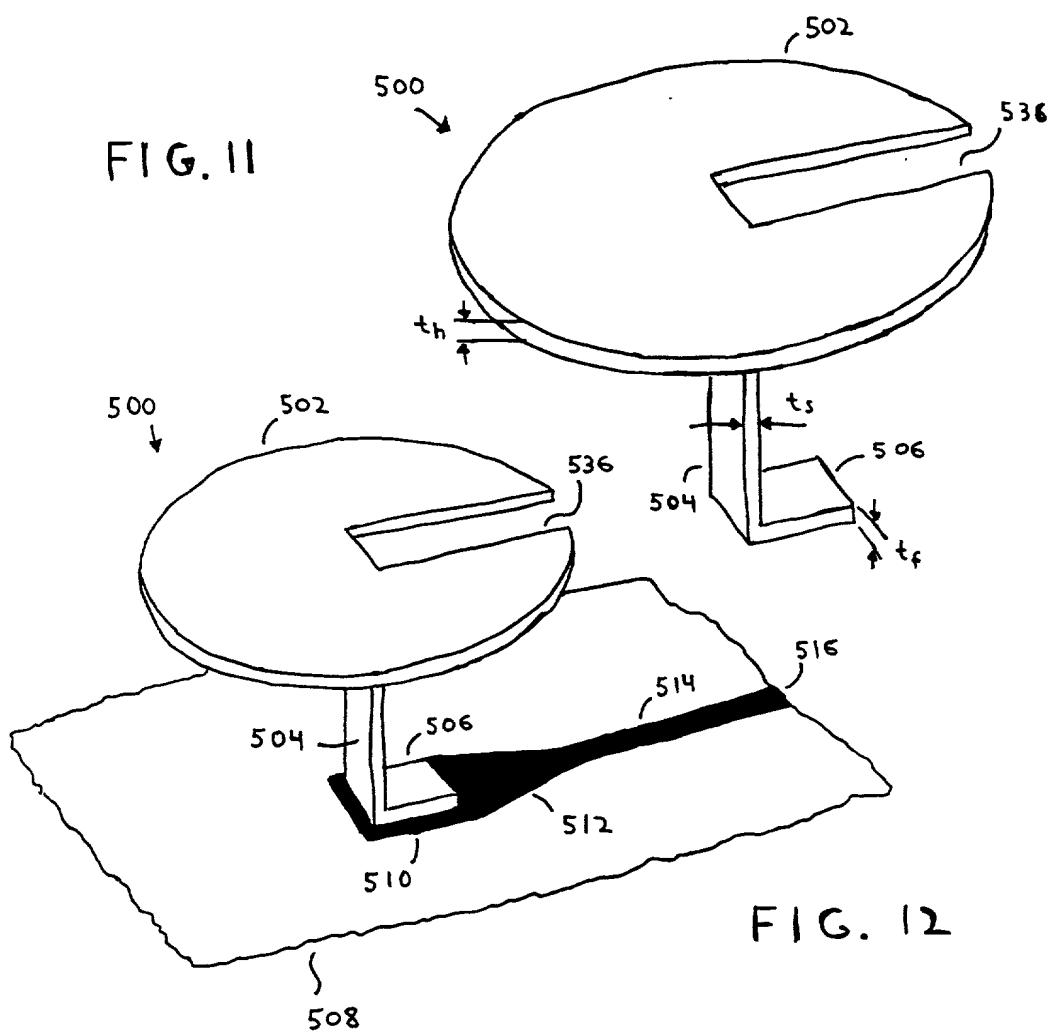


FIG. 12

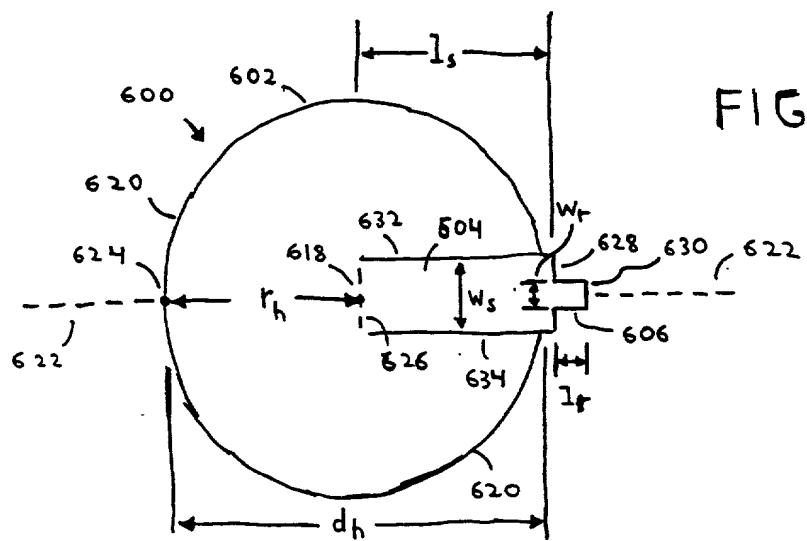


FIG. 13

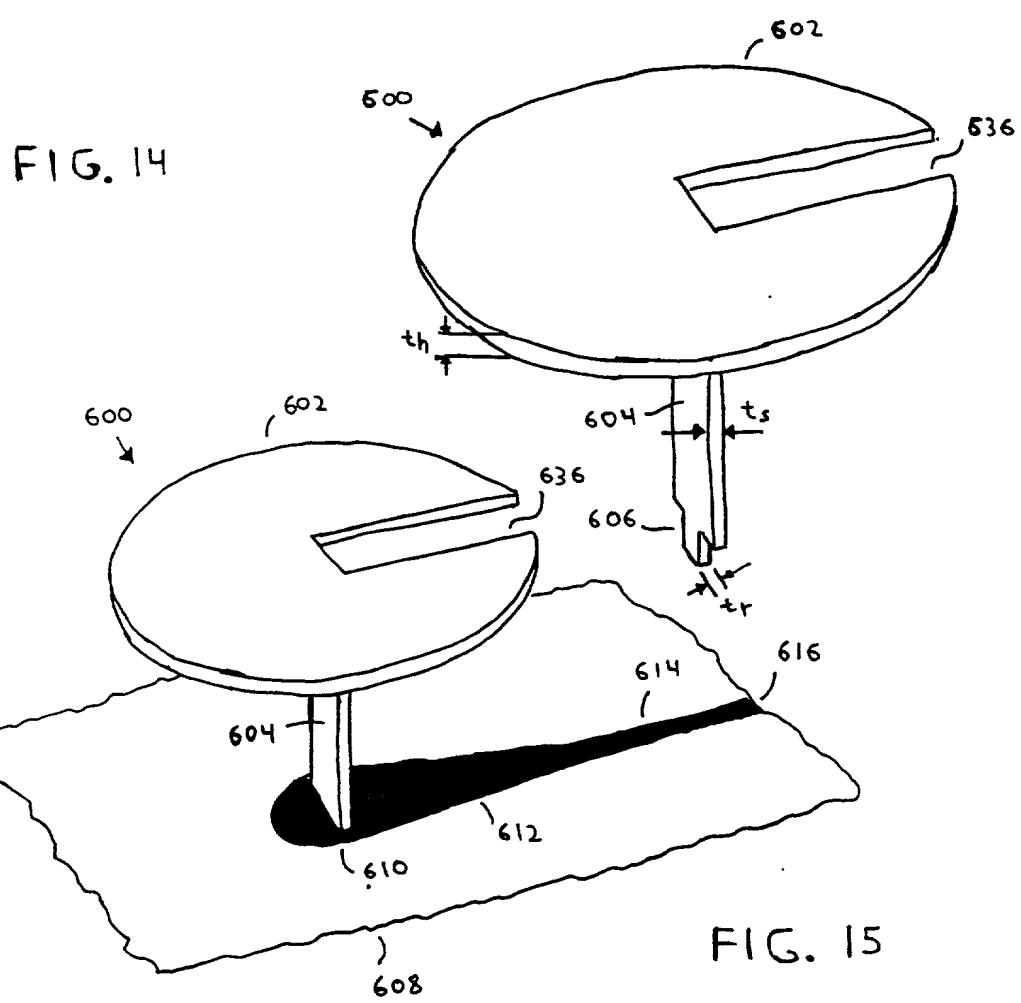


FIG. 15

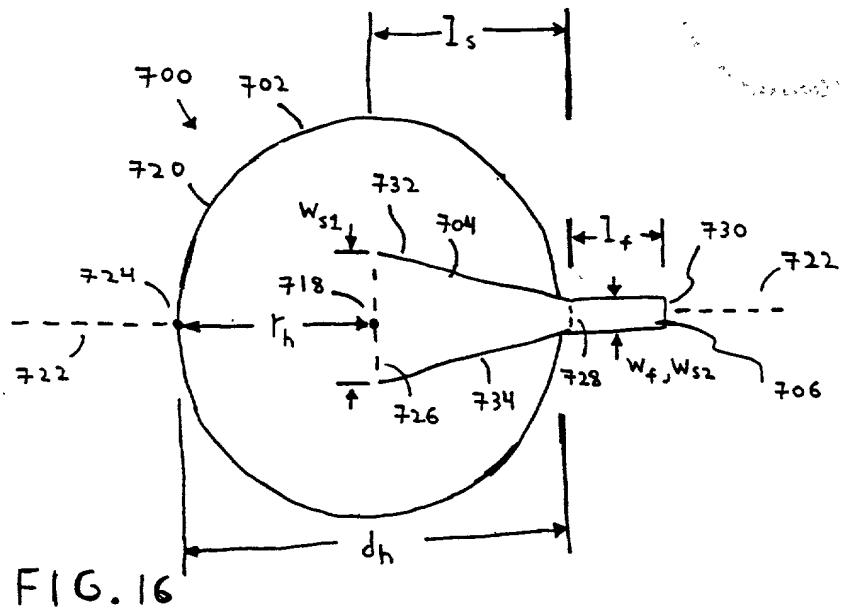


FIG. 16

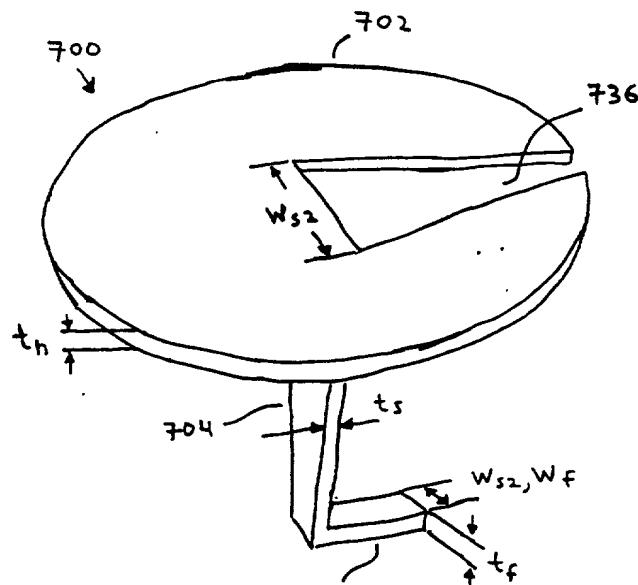


FIG. 17

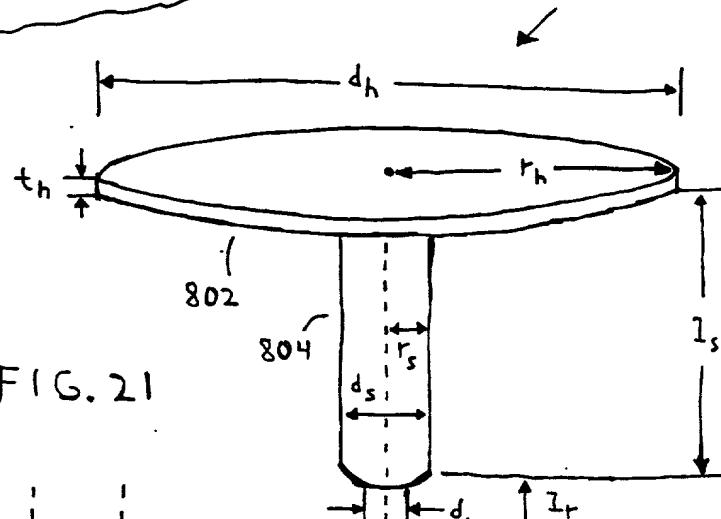
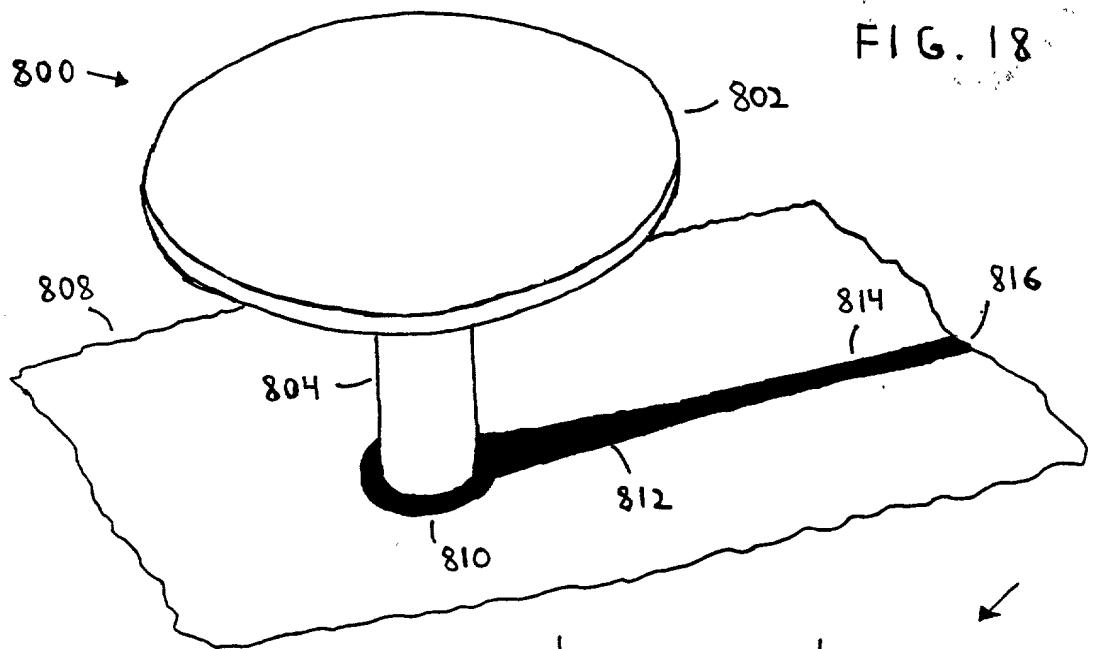


FIG. 20

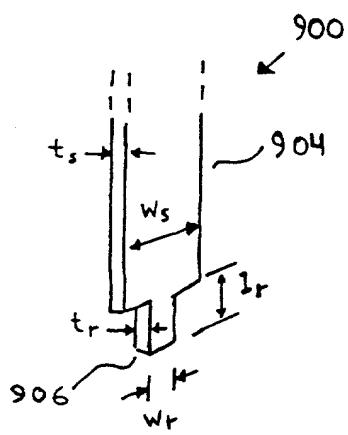


FIG. 21

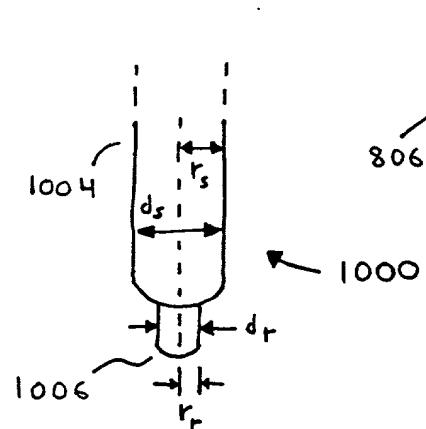


FIG. 19

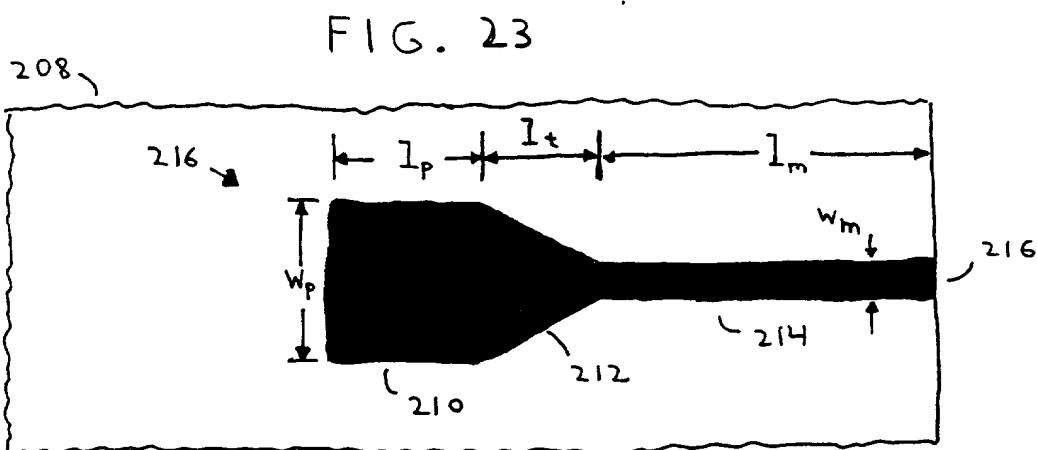
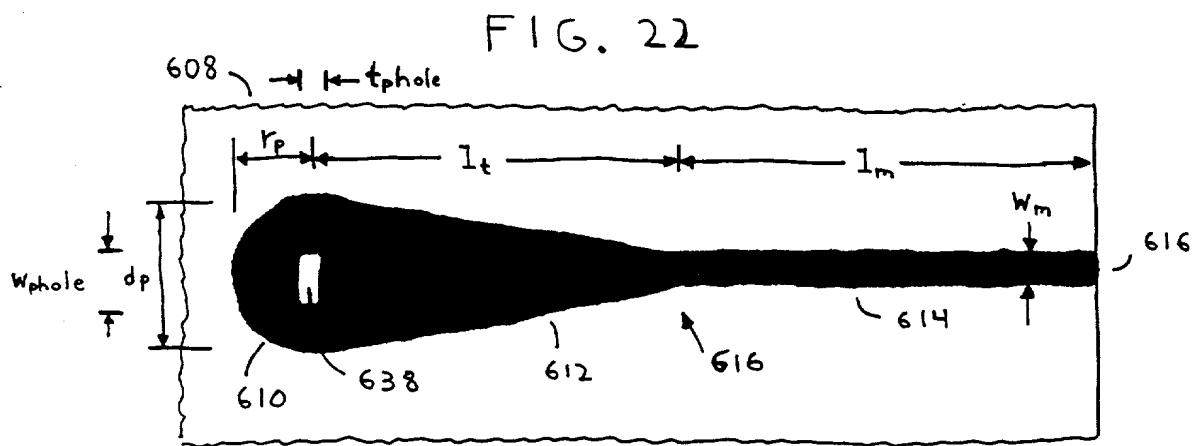
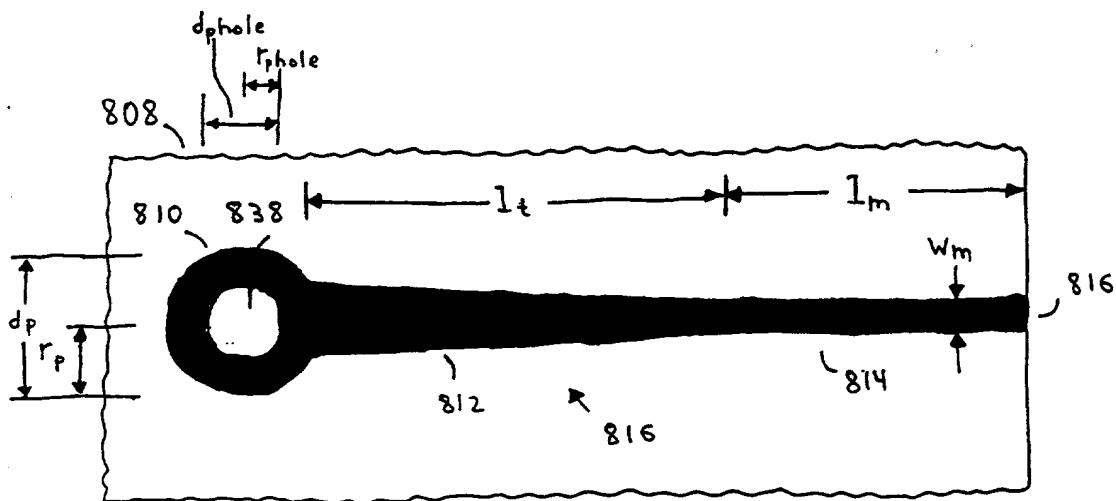


FIG. 24

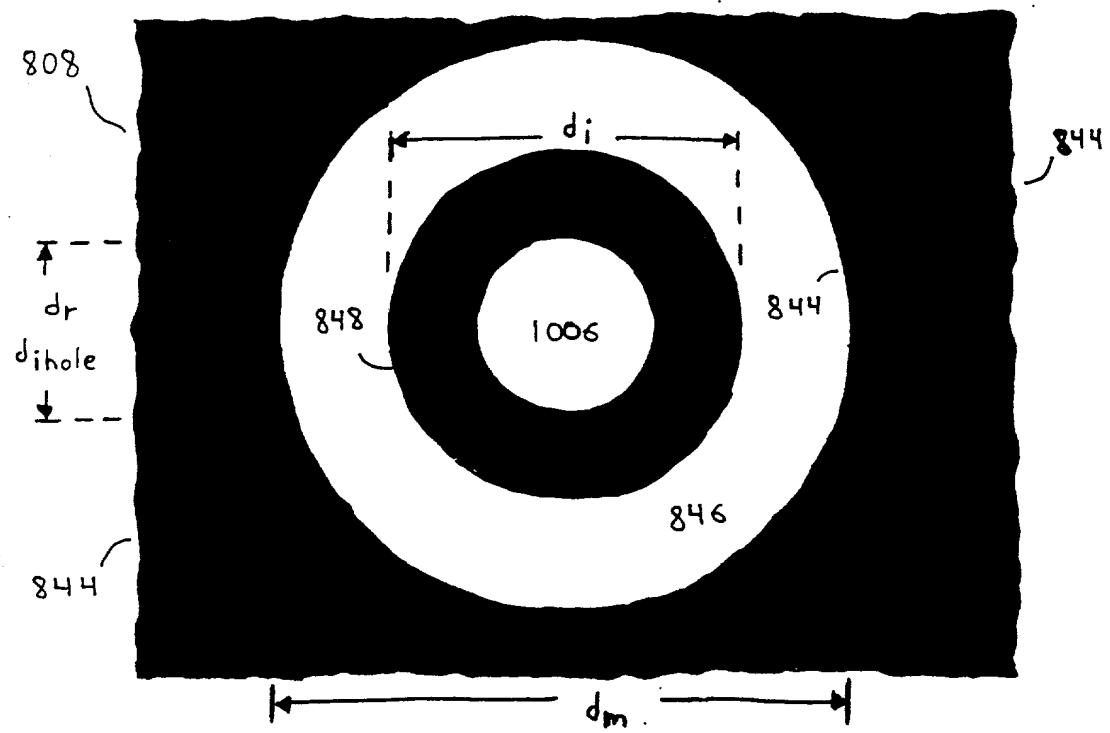
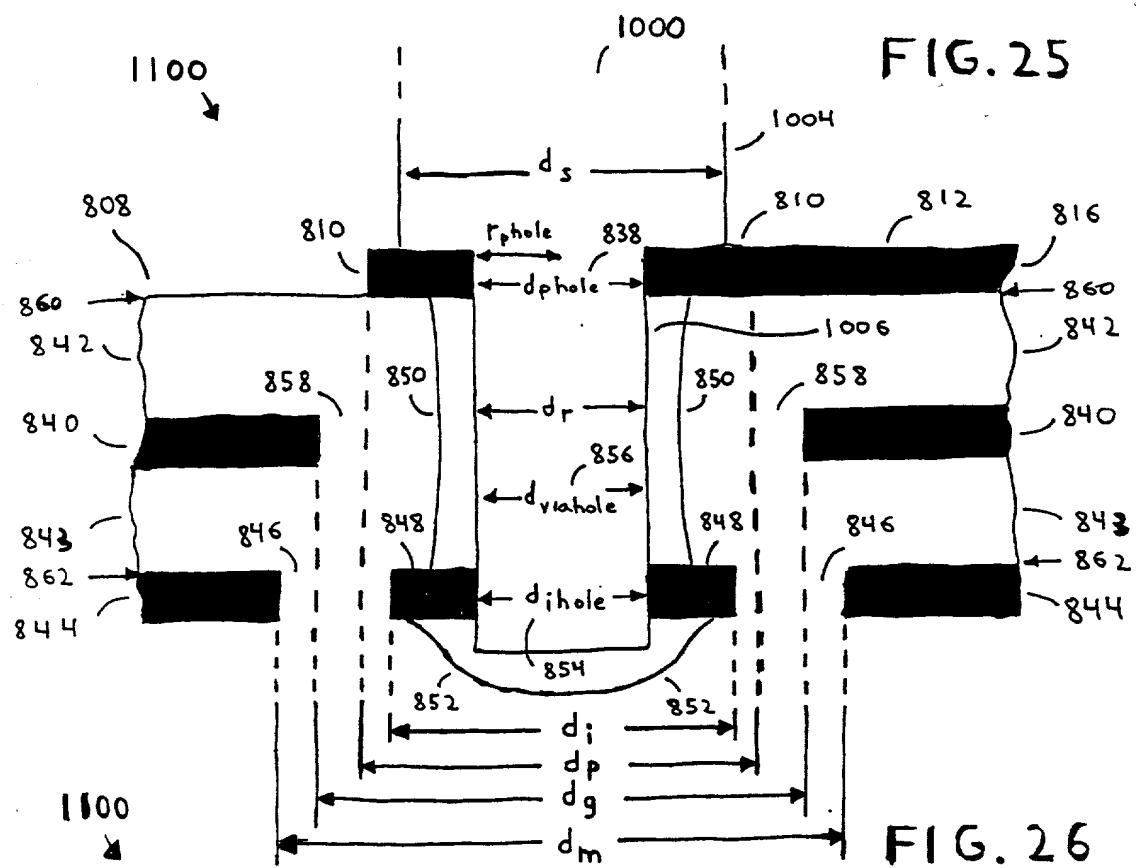
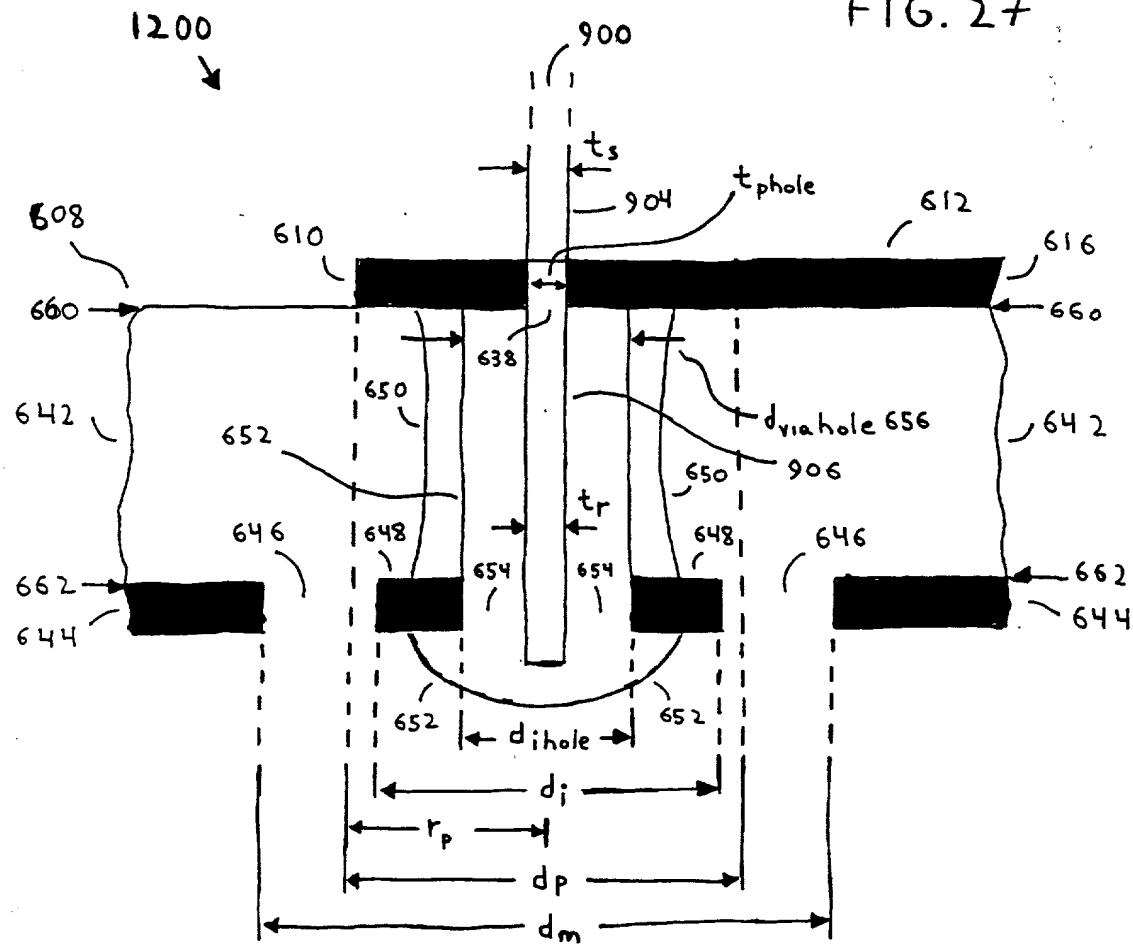


FIG. 27



Input Impedance

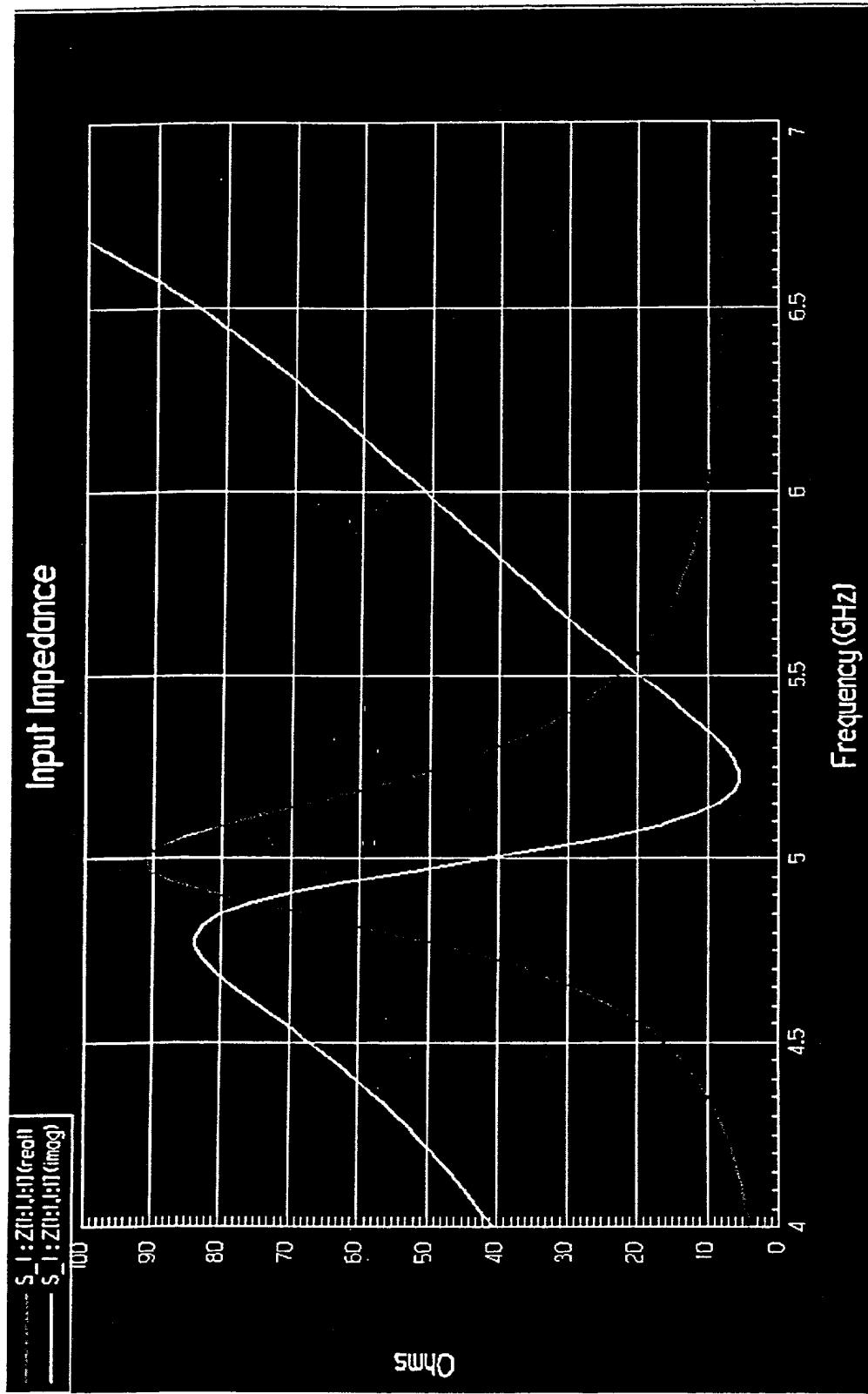


FIG. 28

50 ohm VSWR

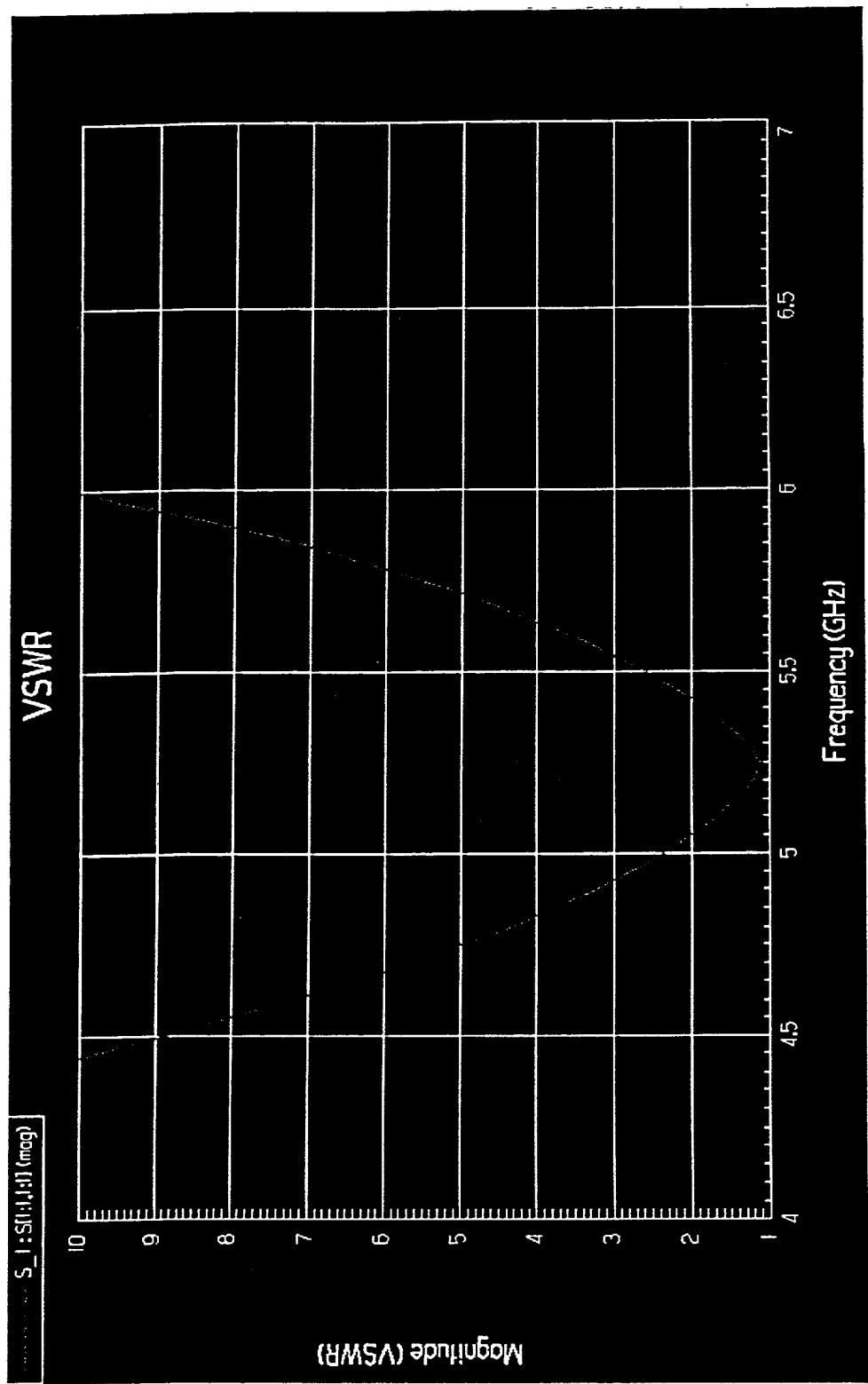


FIG. 29

50 ohm VSWR

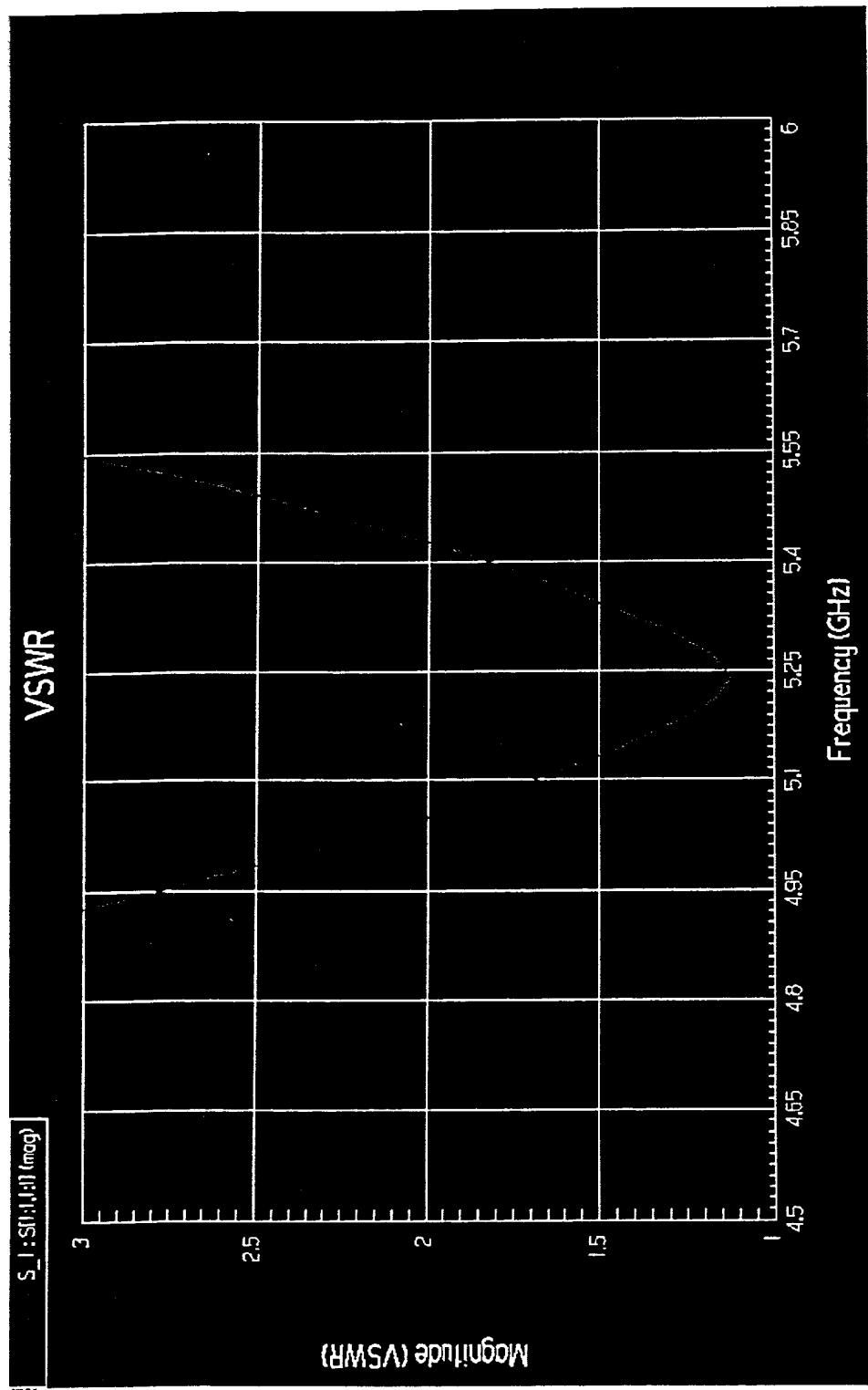


FIG. 30

Input Impedance

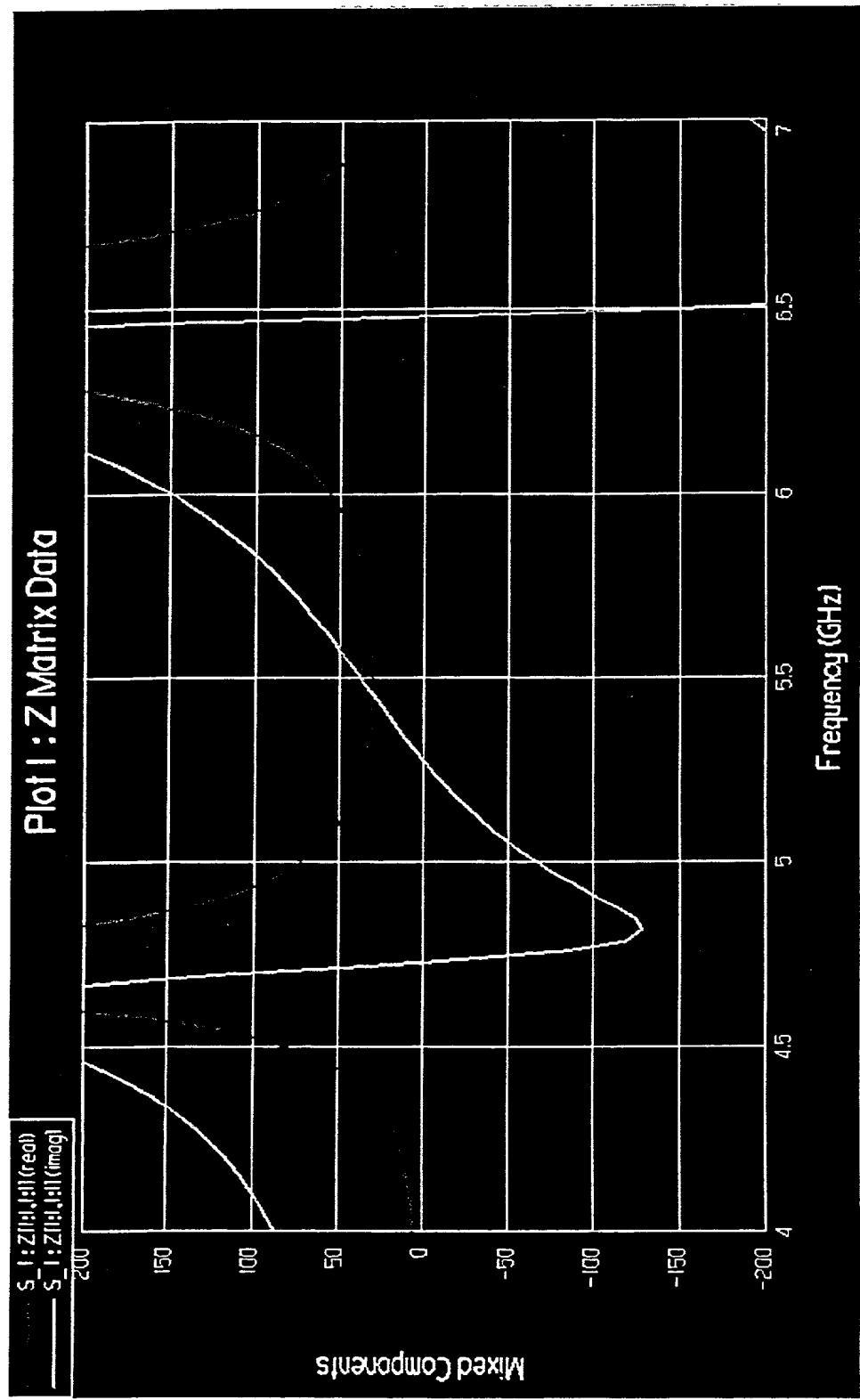


FIG. 31

50 ohm VSWR

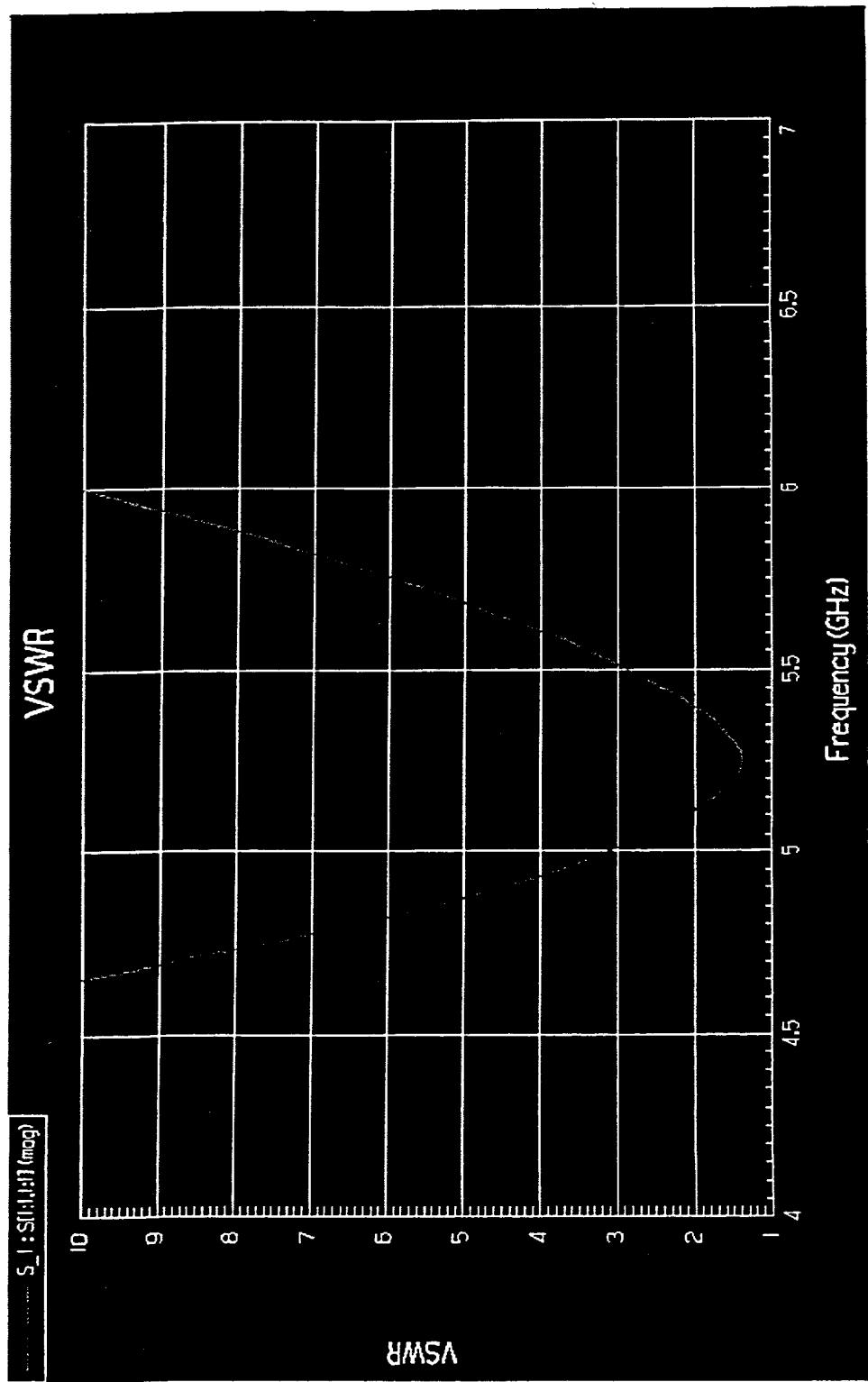


FIG. 32

50 ohm VSWR

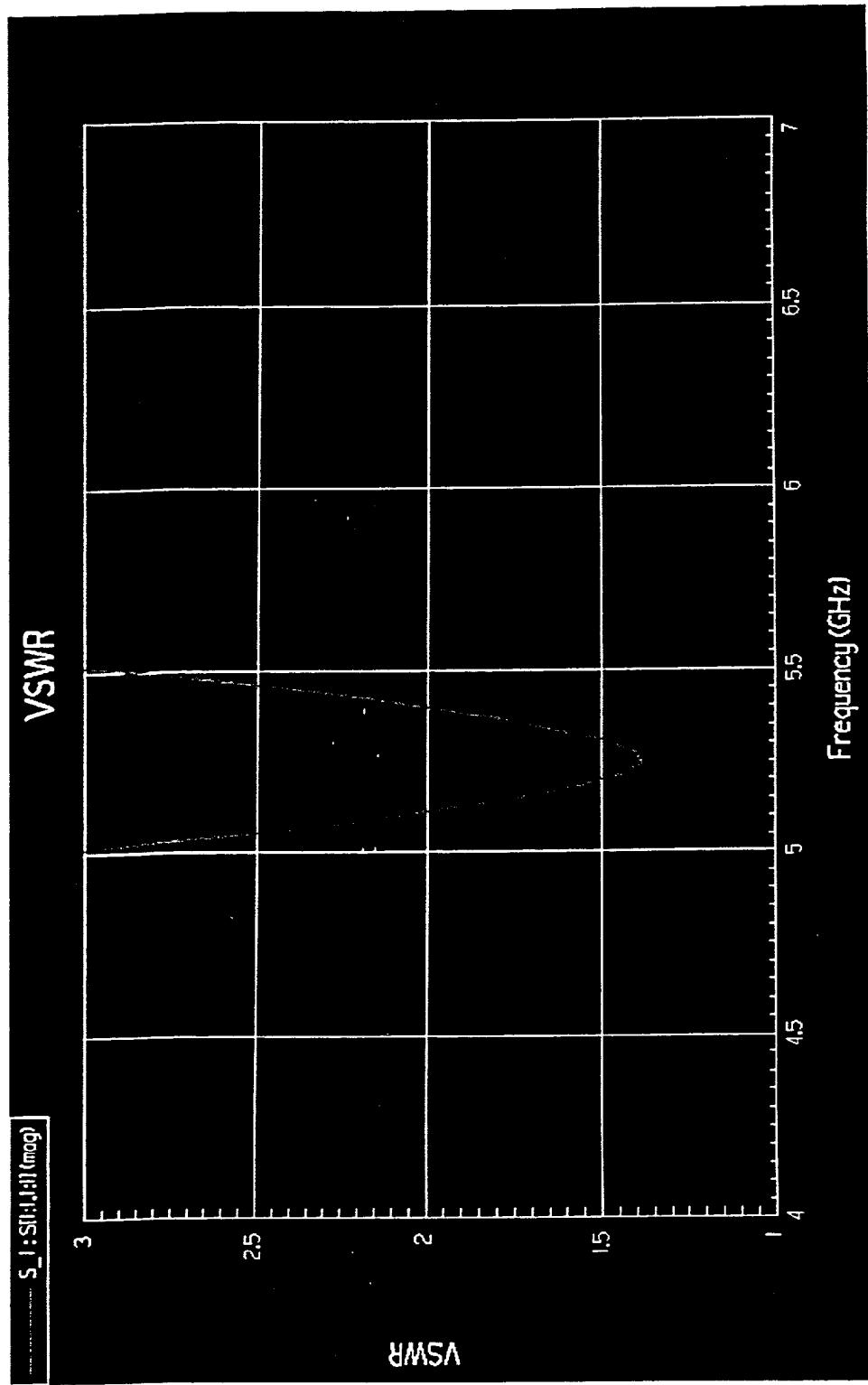


FIG. 33

Input Impedance

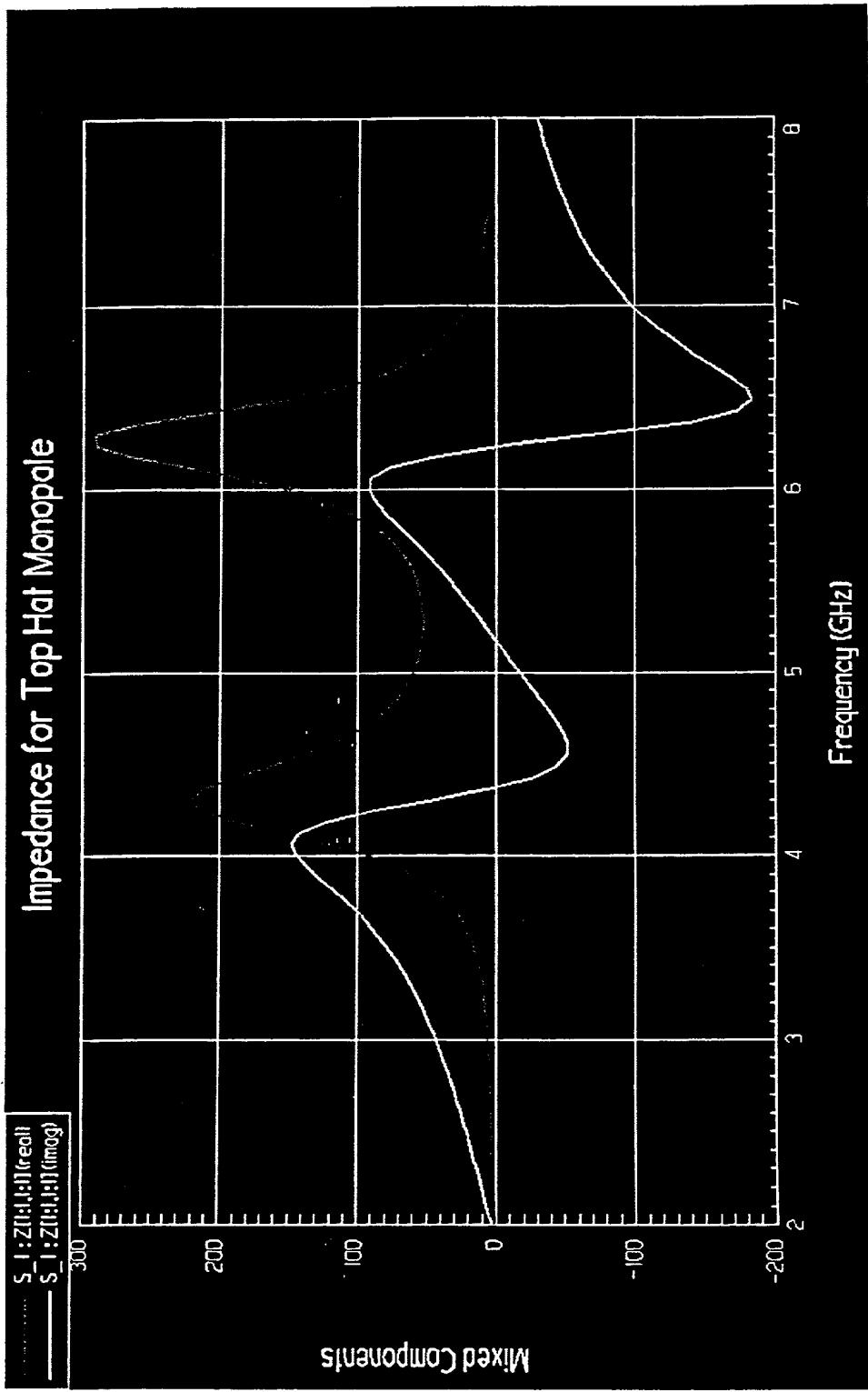


FIG. 34

Input Impedance

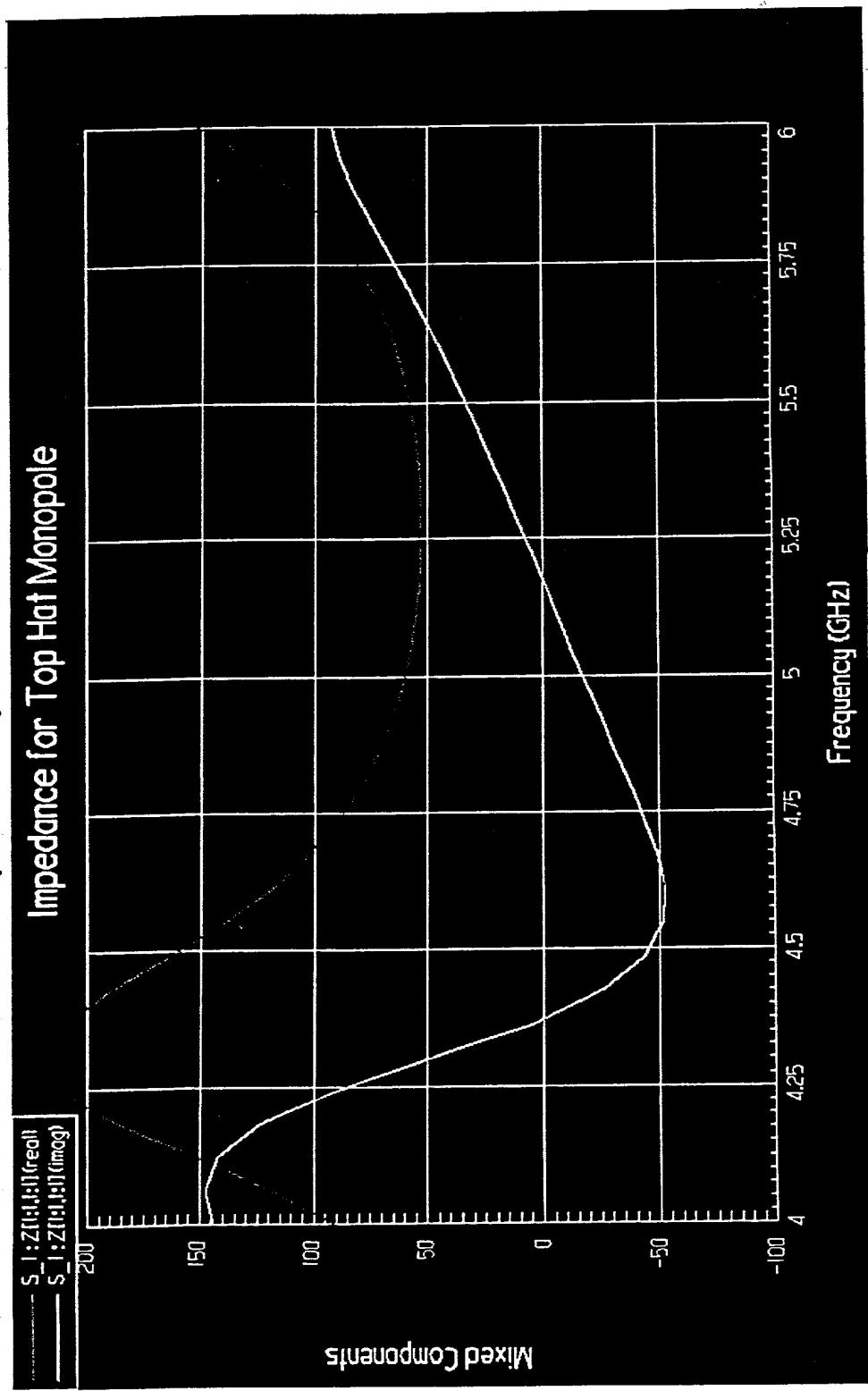


FIG. 35

50 ohm VSWR

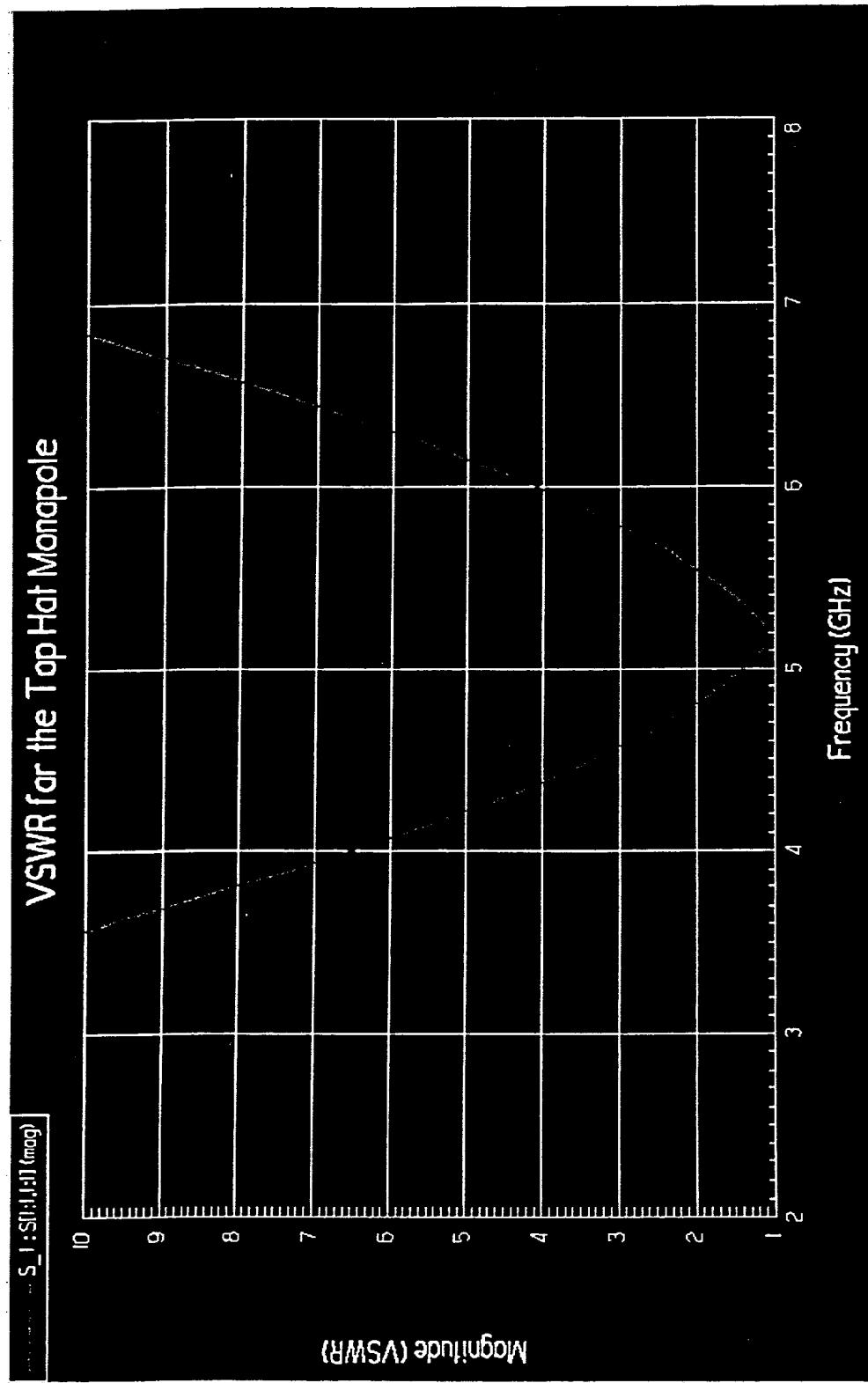


FIG. 36

50 ohm VSWR

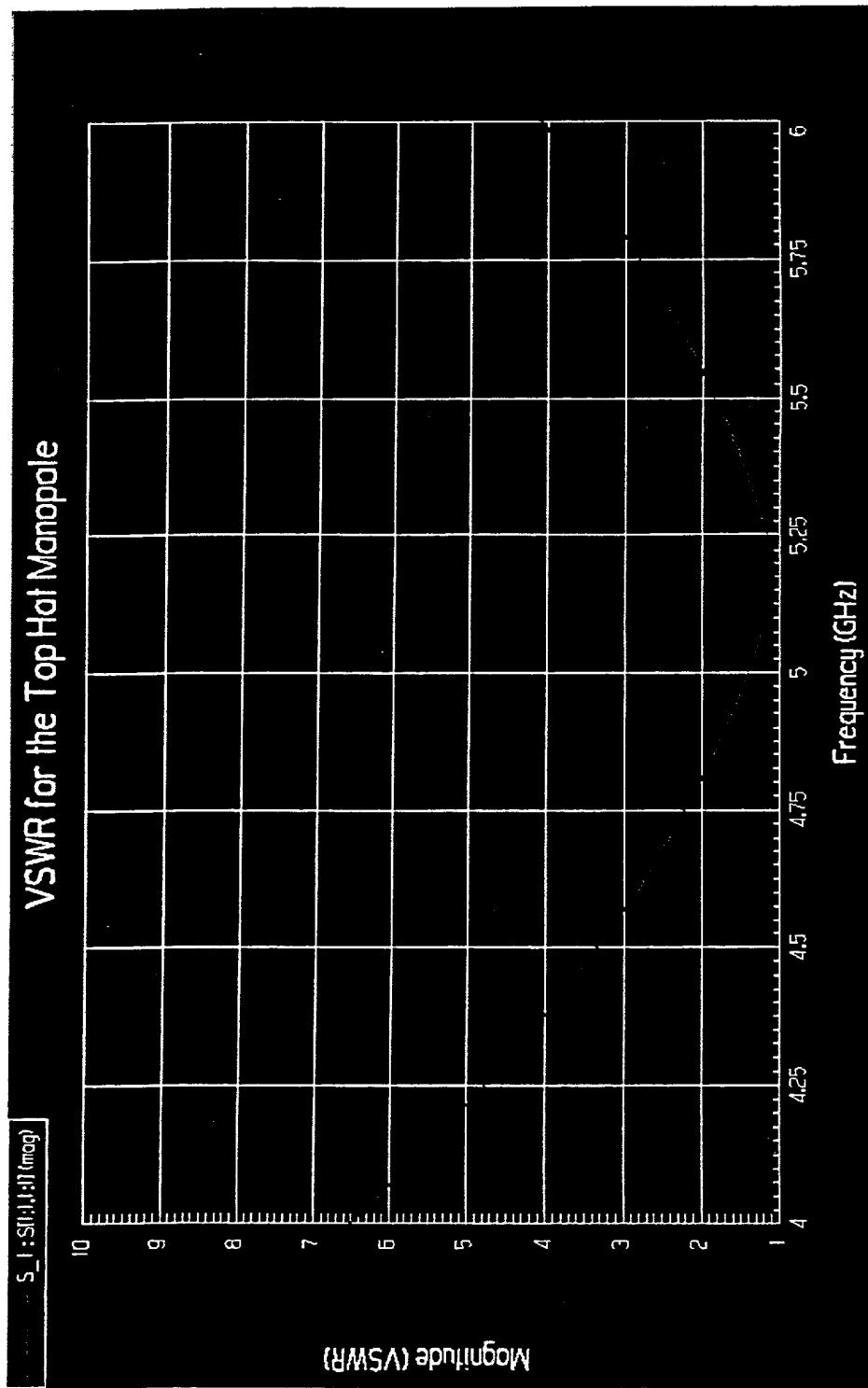


FIG. . 37